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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Kazuhiro Nakajima

Serial No.: 10/649,833

Group Art Unit: 2817

Filing Date: August 28, 2003

Examiner: Shingleton, Michael B.

For: TEST METHOD AND APPARATUS FOR VERIFYING FABRICATION OF
TRANSISTORS IN AN INTEGRATED CIRCUIT

Honorable Commissioner for Patents
Alexandria, VA 22313-1450

REQUEST FOR CONSIDERATION OF PRIORITY DOCUMENT

Sir:


Applicant respectfully requests that the priority document submitted and filed on August 28, 2003, be officially considered of record.

A Notice of Allowability was issued on June 17, 2005. However, the Notice of Allowability did not acknowledge the priority document.

Therefore, Applicant respectfully requests that the Examiner officially acknowledge the priority document submitted on August 28, 2003.

Respectfully Submitted,

Date: June 21, 2005


James N. Dresser, Esq.
Registration No. 22,973

Sean M. McGinn, Esq.
Registration No. 34,386

McGinn & Gibb, PLLC
8321 Old Courthouse Road, Suite 200
Vienna, VA 22182-3817
(703) 761-4100
Customer No. 21254